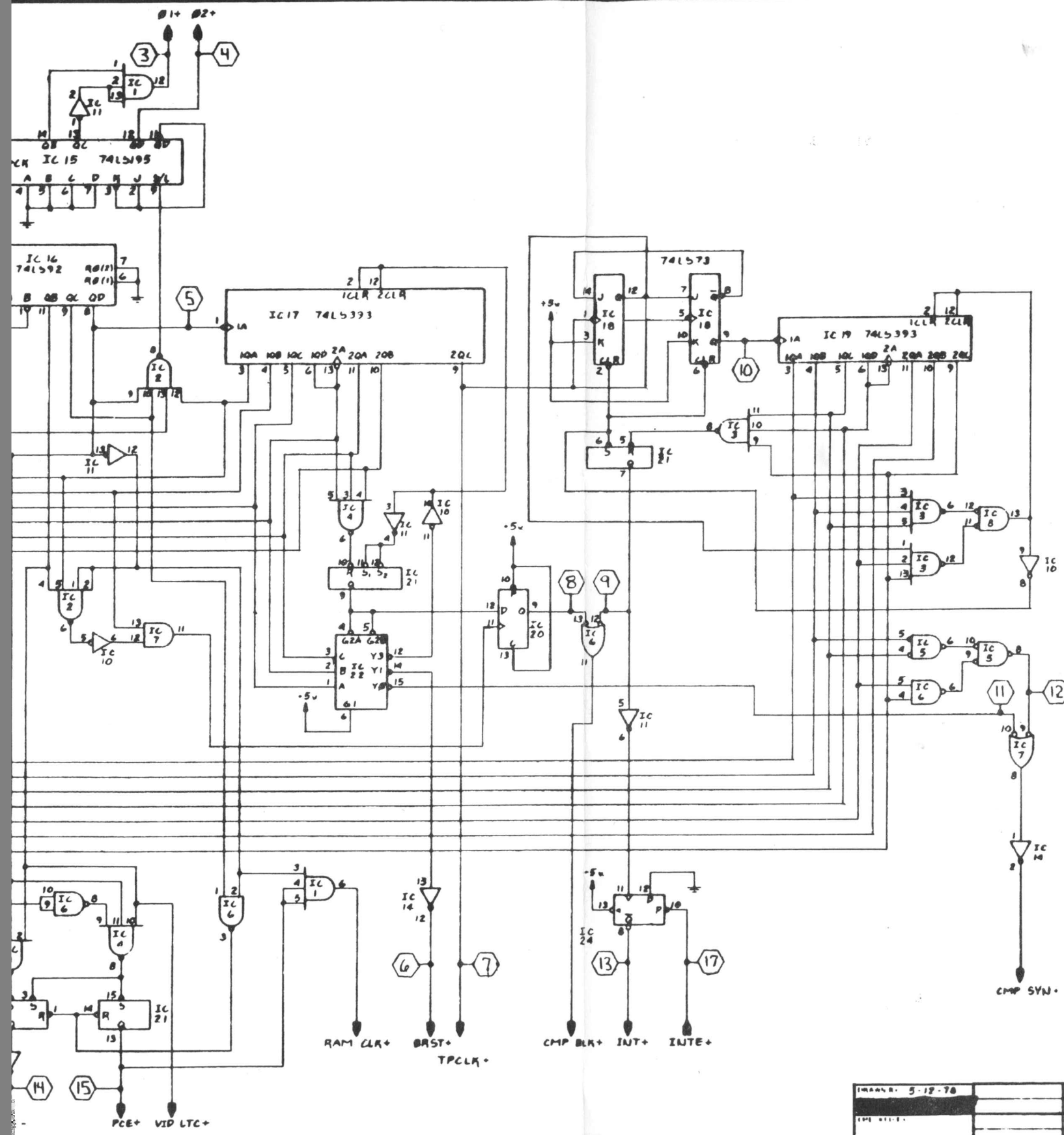


14.3 8180 MHz

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
REL FOR PROD			

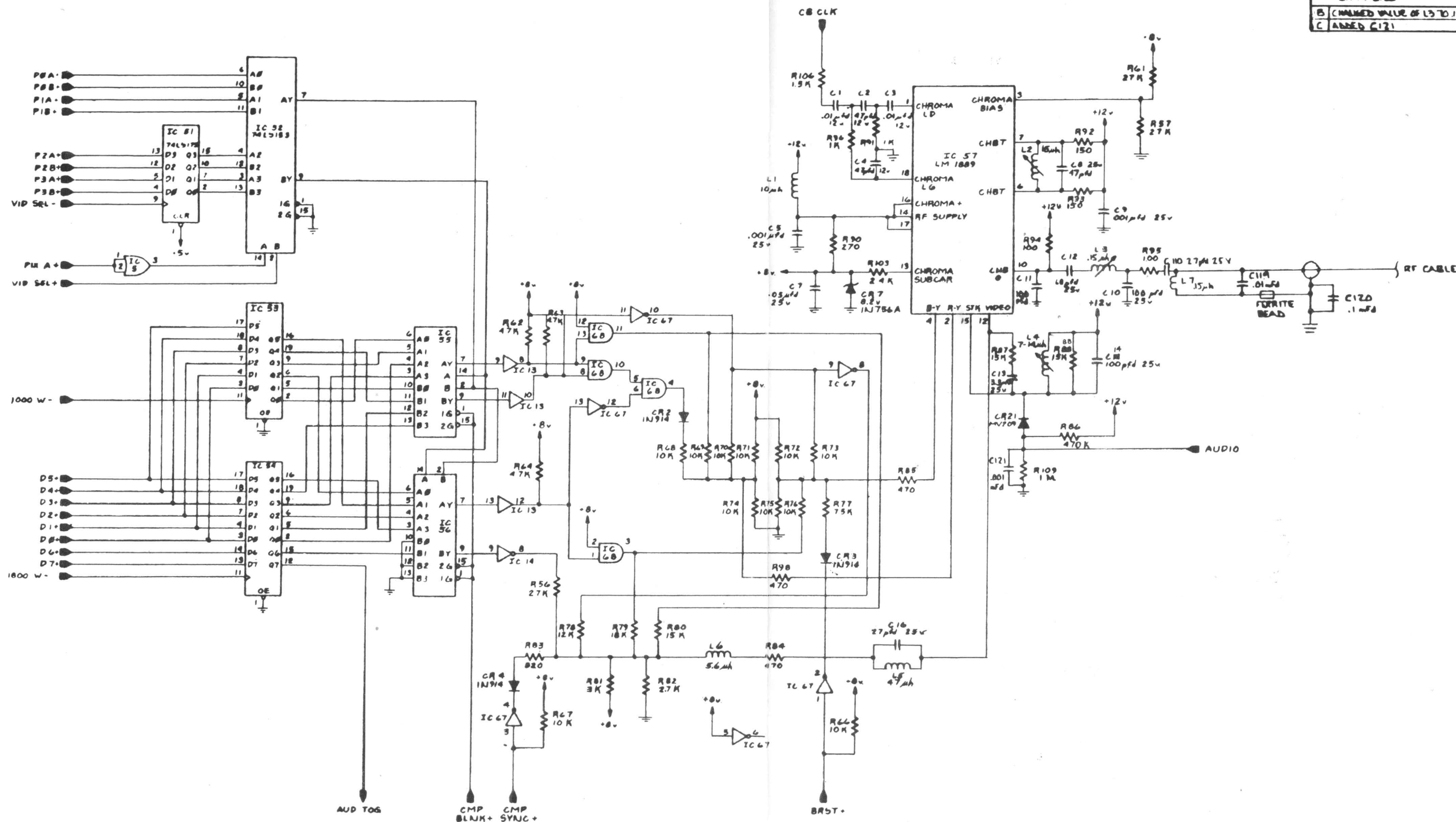


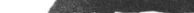
1.323 45

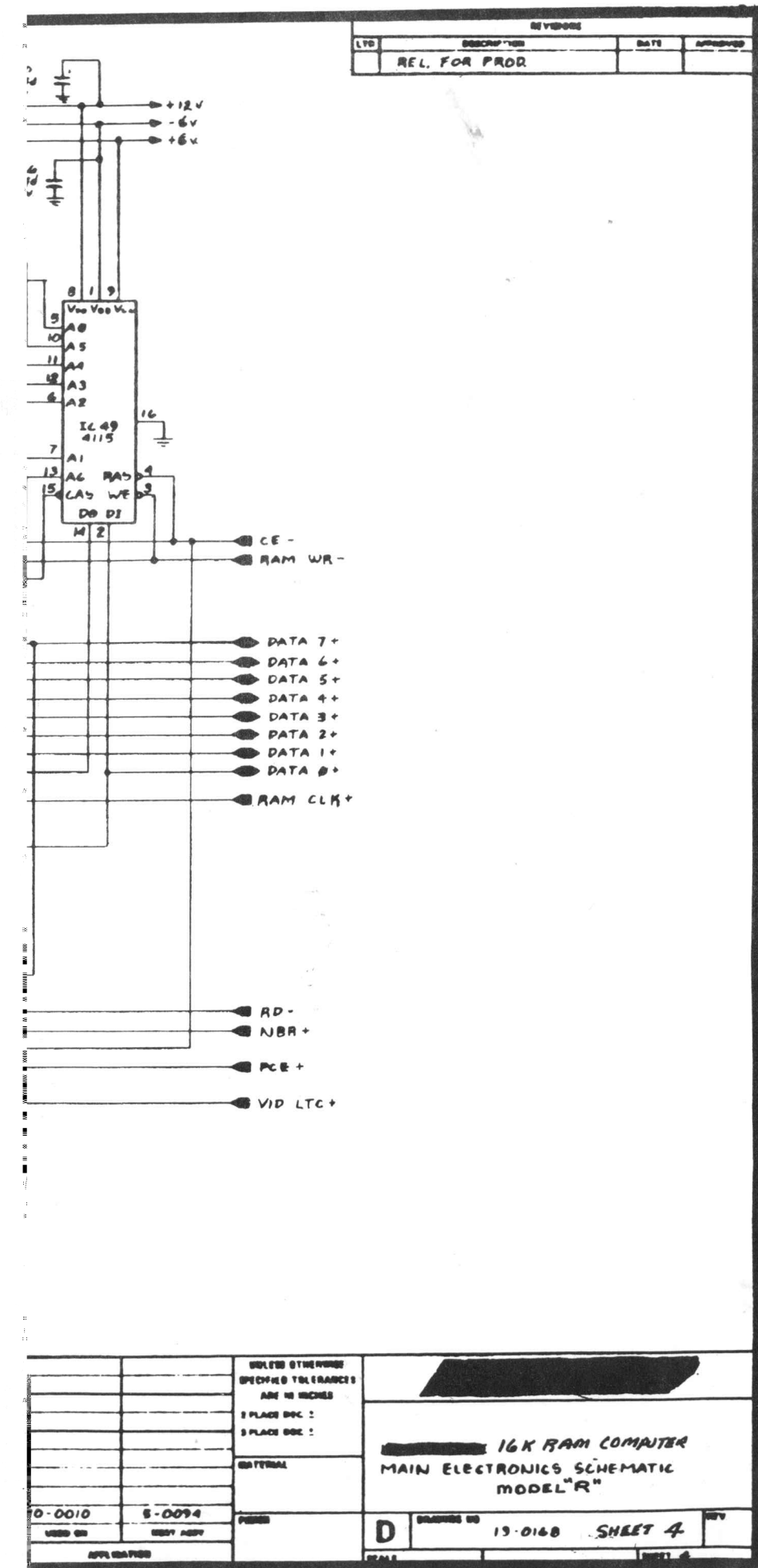
MEASUREMENT POINTS

DRAWN: 5-12-78 CHECKED: APPROVED:		UNLESS OTHERWISE SPECIFIED TOLERANCES ARE IN INCHES 3 PLACE DEC. 3 PLACE DEC.		16K RAM COMPUTER MAIN ELECTRONICS SCHEMATIC MODEL "R"	
10-0010 USED ON:		5-0094 USED ON:		D DRAWING NO: 13 016 SHEET 2	

REVISIONS			
1. NO	DESCRIPTION	DATE	APPROVED
	REL FOR PROD		
A	CHANGED C6 TO C11 TO 100 PPS + ADDED C119 + C120	B-24-78	
B	CHANGED VALUE OF C3 TO 15	B-26-78	
C	ADDED C121	9-1-78	



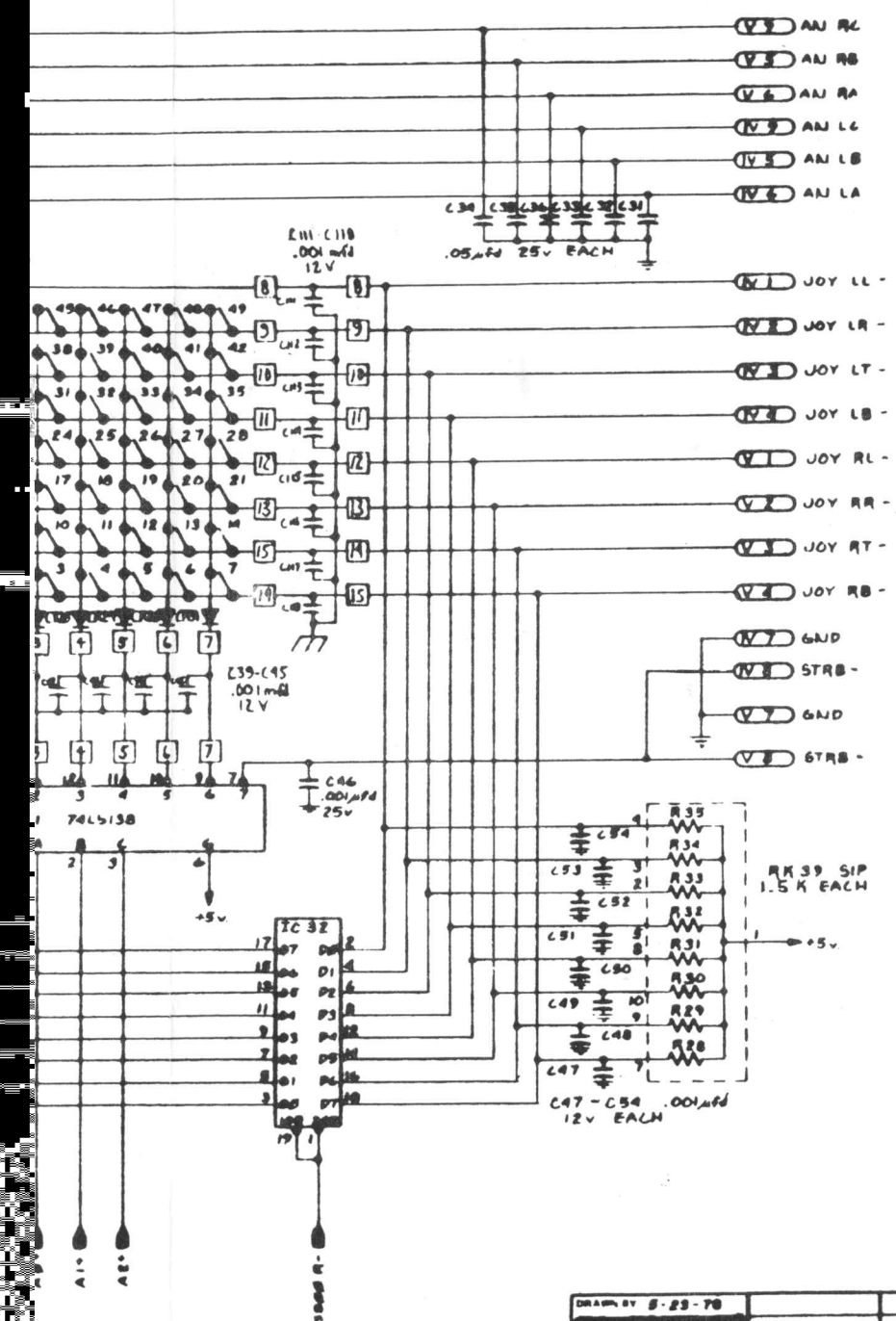
DRAWING BY 5-17-78 DESIGNED BY APPROVED	USED SEE STANDARD SPECIFIED TOLERANCES ARE IN INCHES 3 PLACE DEC : 5 PLACE DEC : MATERIAL PARTS	  MAIN ELECTRONICS SCHEMATIC MODEL "R"	DRAWING NO D	13-0168	SHEET 3	REV C
10-0010 5-0094 USED OR NEW KEY APPLICATION			SCALE		SHEET 3	



LTD	DESCRIPTION	DATE	APPROVED
	REL. FOR PROD.		

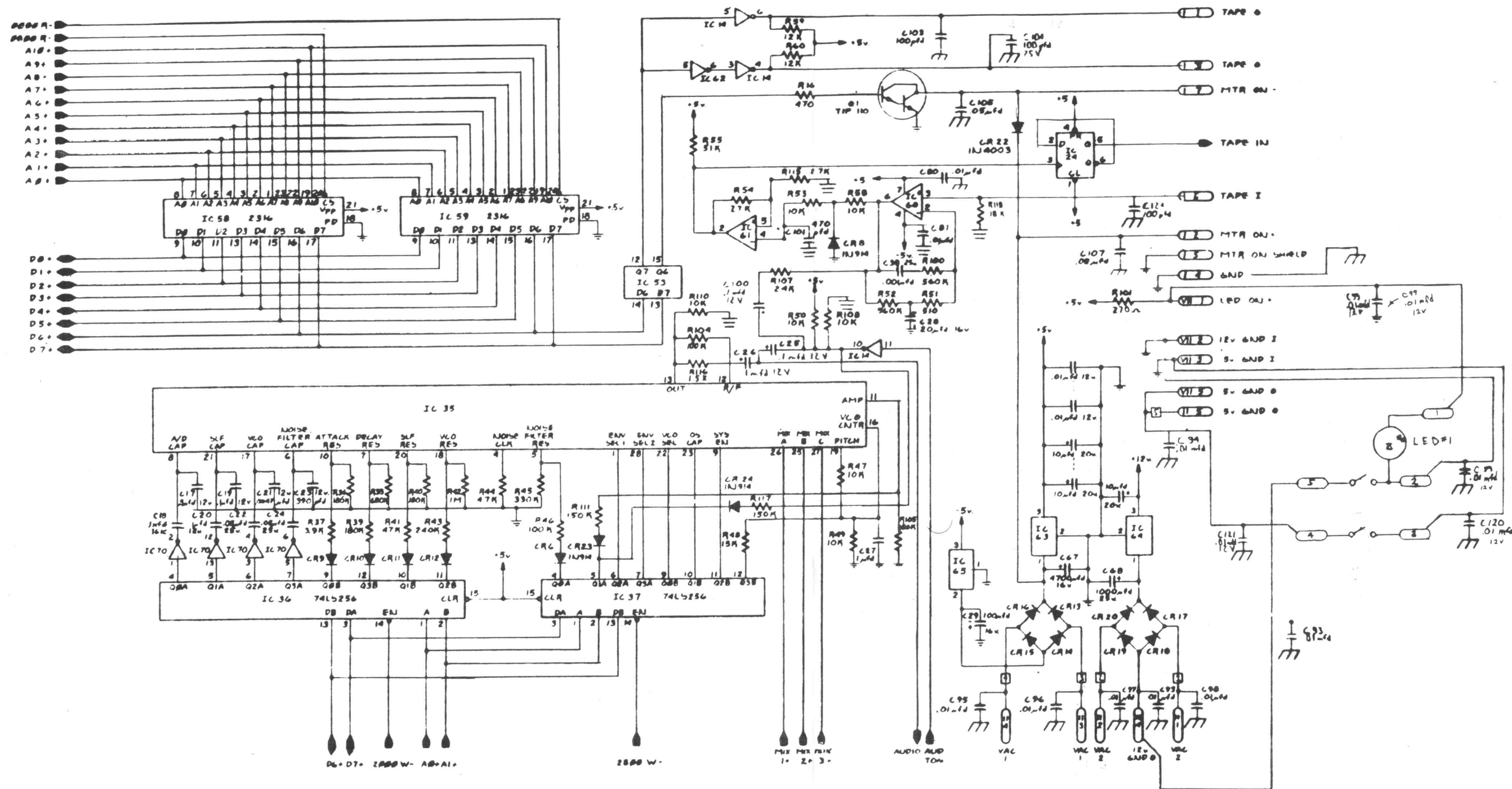
UNLESS OTHERWISE SPECIFIED TOLERANCES ARE AS SHOWN		16K RAM COMPUTER	
3 PLACE DEC :		MAIN ELECTRONICS SCHEMATIC	
3 PLACE DEC :		MODEL "R"	
D-0010		D	
S-0094		19-0168	
USED ON		SHEET 4	
APPROVED		SCALE	

REVISION			
LTG	DESCRIPTION	DATE	APPROVED
1	REL FOR PROR	7 18 78	5-5



DRAWN BY 5-23-78		UNLESS OTHERWISE SPECIFIED TOLERANCES ARE AS SHOWN		16K RAM COMPUTER MAIN ELECTRONICS SCHEMATIC MODEL "A"
CHECKED BY		3 PLANE DSC 1	3 PLANE DSC 2	
APPROVED		EXTERNAL		D 13-016A SHEET 5
10-0010	5-0094	POWER		
REVISION	REVISION	REVISION		REVISION

REVISION			
LTB	DESCRIPTION	DATE	APPROVED
	REL. FOR PROD.		
A	C20 CHANGED TO 10 IN. I.D.	9-1-78	B
B	DELETE 2 ND C98 ^{ASST} C 113 - C 124		
C	ADD C 125 TO C 128. A.S. C 113 TO C 124		
	ADD C 129 TO C 128		



$\perp$ GROUND ON MAIN BOARD

 GROUND ON INTERCONNECT BD.

[illegible]